

## DETAILED ACTION

### *Drawings*

1. The drawings were received on 08/06/2010. These drawings are accepted.

## EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

**Claims 11, 14-17, and 23-27** are allowable. **Claims 20-21**, previously withdrawn from consideration as a result of a restriction requirement, requires all the limitations of an allowable claim. Pursuant to the procedures set forth in MPEP § 821.04(a), **the restriction requirement between inventions of species I and II, as set forth in the Office action mailed on 10/30/2008, is hereby withdrawn** and **claims 20-21** are hereby rejoined and fully examined for patentability under 37 CFR 1.104. In view of the withdrawal of the restriction requirement, applicant(s) are advised that if any claim presented in a continuation or divisional application is anticipated by, or includes all the limitations of, a claim that is allowable in the present application, such claim may be subject to provisional statutory and/or nonstatutory double patenting rejections over the claims of the instant application. Once the restriction requirement is withdrawn, the

provisions of 35 U.S.C. 121 are no longer applicable. See *In re Ziegler*, 443 F.2d 1211, 1215, 170 USPQ 129, 131-32 (CCPA 1971). See also MPEP § 804.01.

3. Authorization for this examiner's amendment below was given in a telephone interview with Robert A. Madsen on 08/27/2010.

**Cancel Claims 1-10 and 21.**

**Re Claim 11**, in line 3, delete "electrodes" and insert -- electrode --. Also, in line 7, delete "electrodes" and insert -- electrode --.

**Re Claim 20**, in lines 2-3, delete "wherein one forms a bottom gate transistor" and insert -- wherein a bottom gate transistor is formed --.

***Allowable Subject Matter***

4. **Claims 11, 14-17, 20, and 23-27** are allowed.

***Reasons for Allowance***

5. The following is an examiner's statement of reasons for allowance:

**Re Claim 11**, the prior art of record, alone or in combination, and to the examiner's knowledge does not teach, disclose, suggest, or render obvious, at least to the skilled artisan, the instant invention regarding a method for producing a transistor for active matrix display, particularly characterized by producing the transistor recited in claim 11 in combination with a step of forming a plasma treated interface on top of said insulator; and a step of forming a microcrystalline silicon film on top of said treated

interface at a temperature between 100 and 400°C using at least a deposition chemical element and a crystallization chemical element, wherein said microcrystalline silicon film comprises a crystalline fraction of above 80% and said microcrystalline silicon film comprises grains of a size between 10 nm and 400 nm; and wherein the transistor has a linear mobility greater than or equal to  $1.5 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ . Claims 14-17, 20, and 23-27, which depend from claim 11, are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANIEL WHALEN whose telephone number is (571)270-3418. The examiner can normally be reached on Monday-Friday, 7:30am to 5:00pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ha Nguyen can be reached on (571) 272-1678. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/D. W./  
Examiner, Art Unit 2829  
08/27/2010

Daniel Whalen

/Ha T. Nguyen/  
Supervisory Patent Examiner, Art Unit 2829